

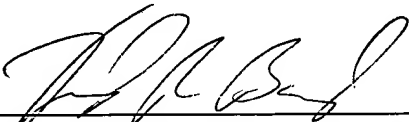
Replace the paragraph beginning on line 18 of page 7 with the following:

Figure 6 shows the part of the circuit that generates the 31st bit carry. Analogous circuit (interconnections) to those shown in Figures 7 and 8 are used for the other bits of the carry result. Once a carry bit has been determined (i.e. the carry-in and structure results in a generate or a kill, with propagate not being possible at that point), then a single signal wire may be used to pass that result to higher levels.

The Commissioner is authorized to charge any additional fees, or credit any overpayment to Deposit Account No. 20-1504 (ITL.0648US).

Respectfully submitted,

Date: 9/27/02



Howard R. Boyle, Reg. No. 29,617  
TROP, PRUNER & HU, P.C.  
8554 Katy Freeway, Suite 100  
Houston, Texas 77024  
713/468-8880 [Phone]  
713/468-8883 [Fax]